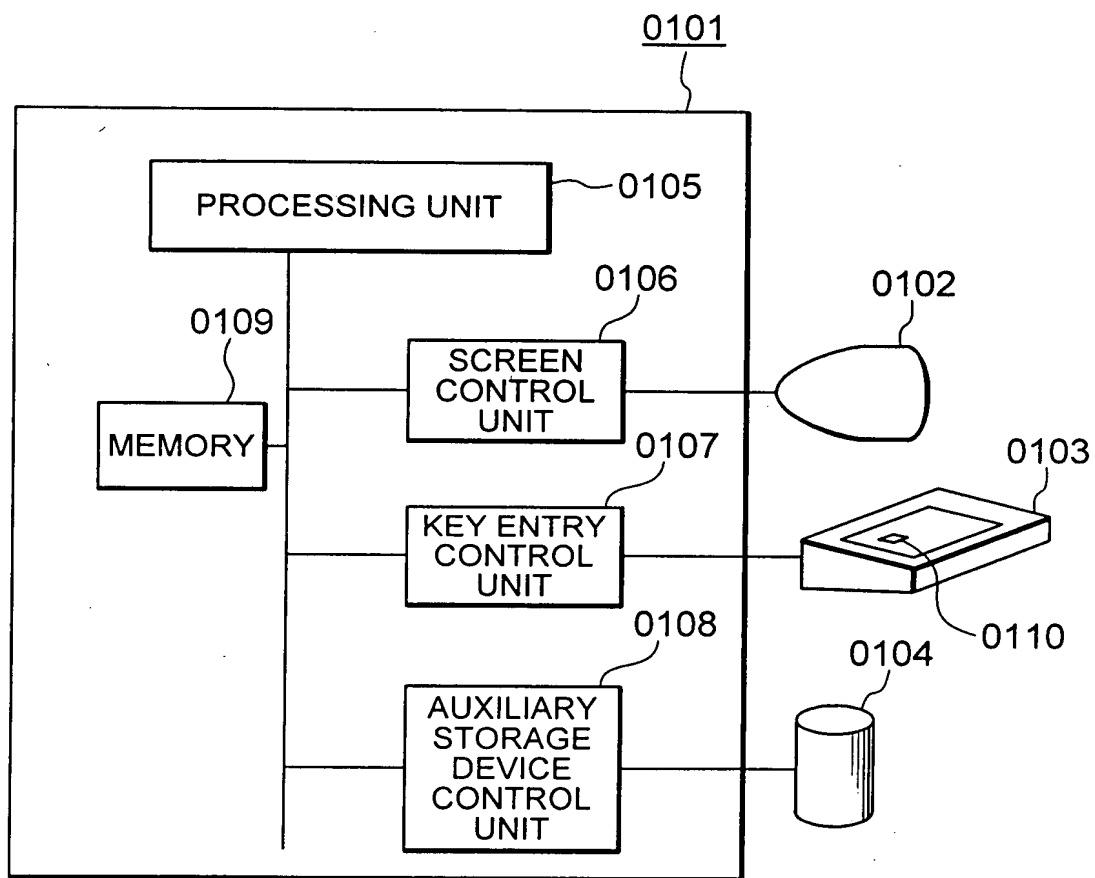
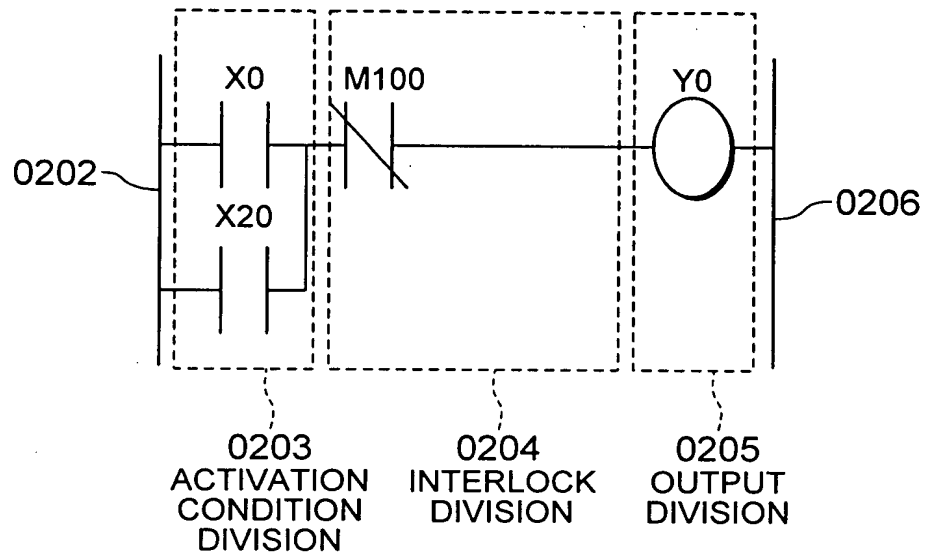


FIG. 1



# FIG. 2

## LADDER DIAGRAM 0201



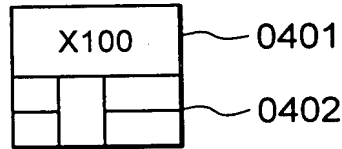
# FIG. 3

## CHARACTER MEMORY 0301

COLUMN		1	2	3	4	5	6	7	8
ROW	1	X0	M100						Y0
2		X20							

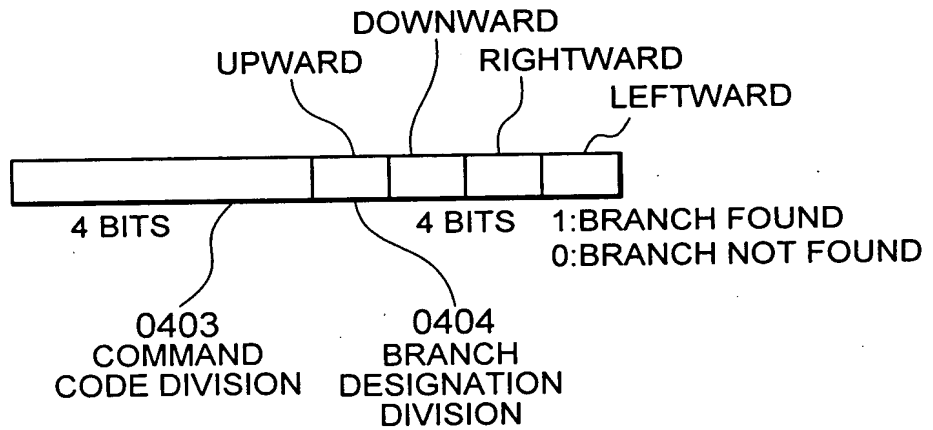
# FIG. 4

STRUCTURE OF EACH LOCATION IN CHARACTER MEMORY



# FIG. 5

STRUCTURE OF SYMBOL AREA 402



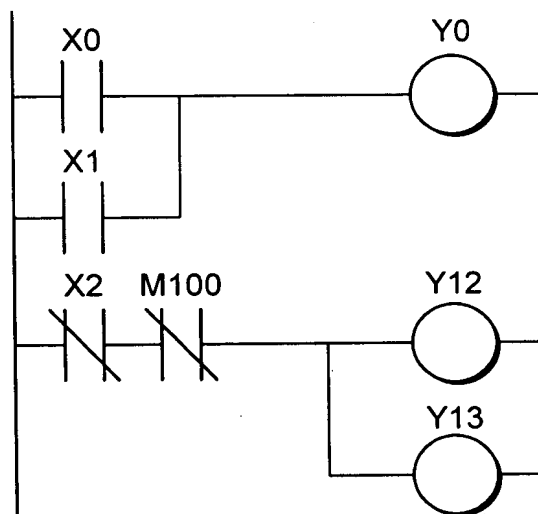
# FIG. 6

CODE TABLE 0405 RELEVANT TO COMMAND CODE DIVISION

COMMAND	COMMAND CODE
	0
	1
	2
CONNECTION	3

# FIG. 7

## LADDER DIAGRAM 0501



# FIG. 8

## CHARACTER MEMORY 0601

COLUMN		1	2	3	4	5	6	7	8
ROW	1	X0							Y0
	2	X1							
	3	X1	M100						Y12
	4								Y13

These three small charts in red ink are the only ones that have been found in the manuscript.

# LADDER DIAGRAM 0701

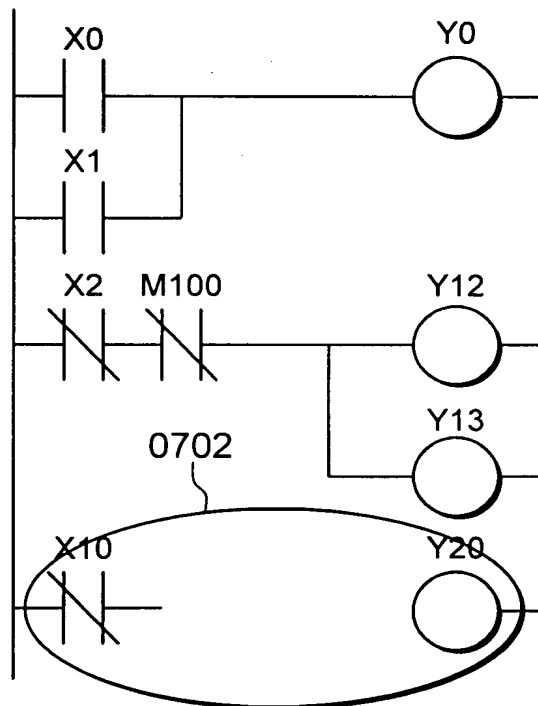


FIG. 10

CHARACTER MEMORY

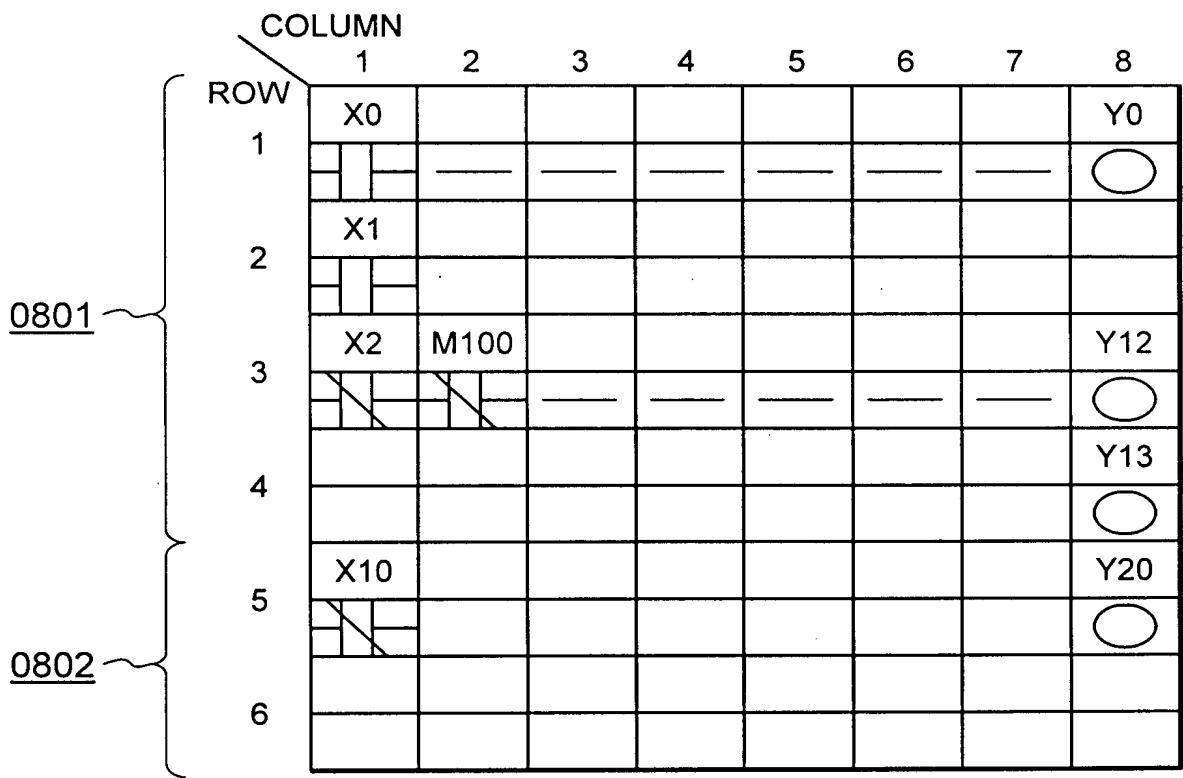


FIG. 11

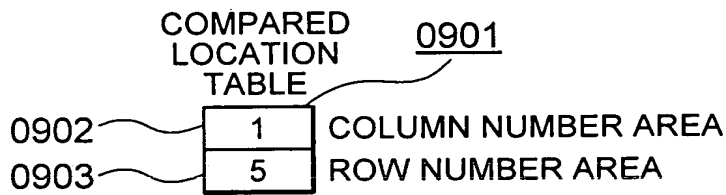


FIG. 12

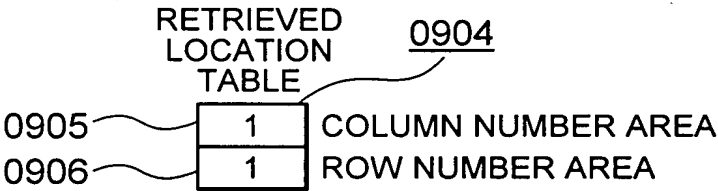
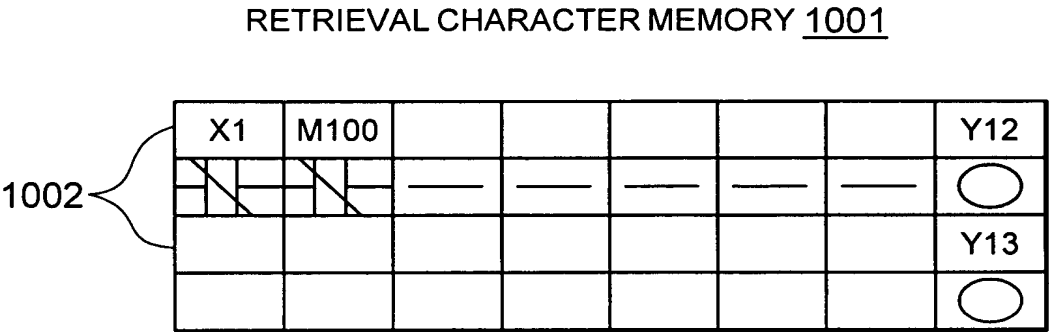


FIG. 13



# FIG. 14

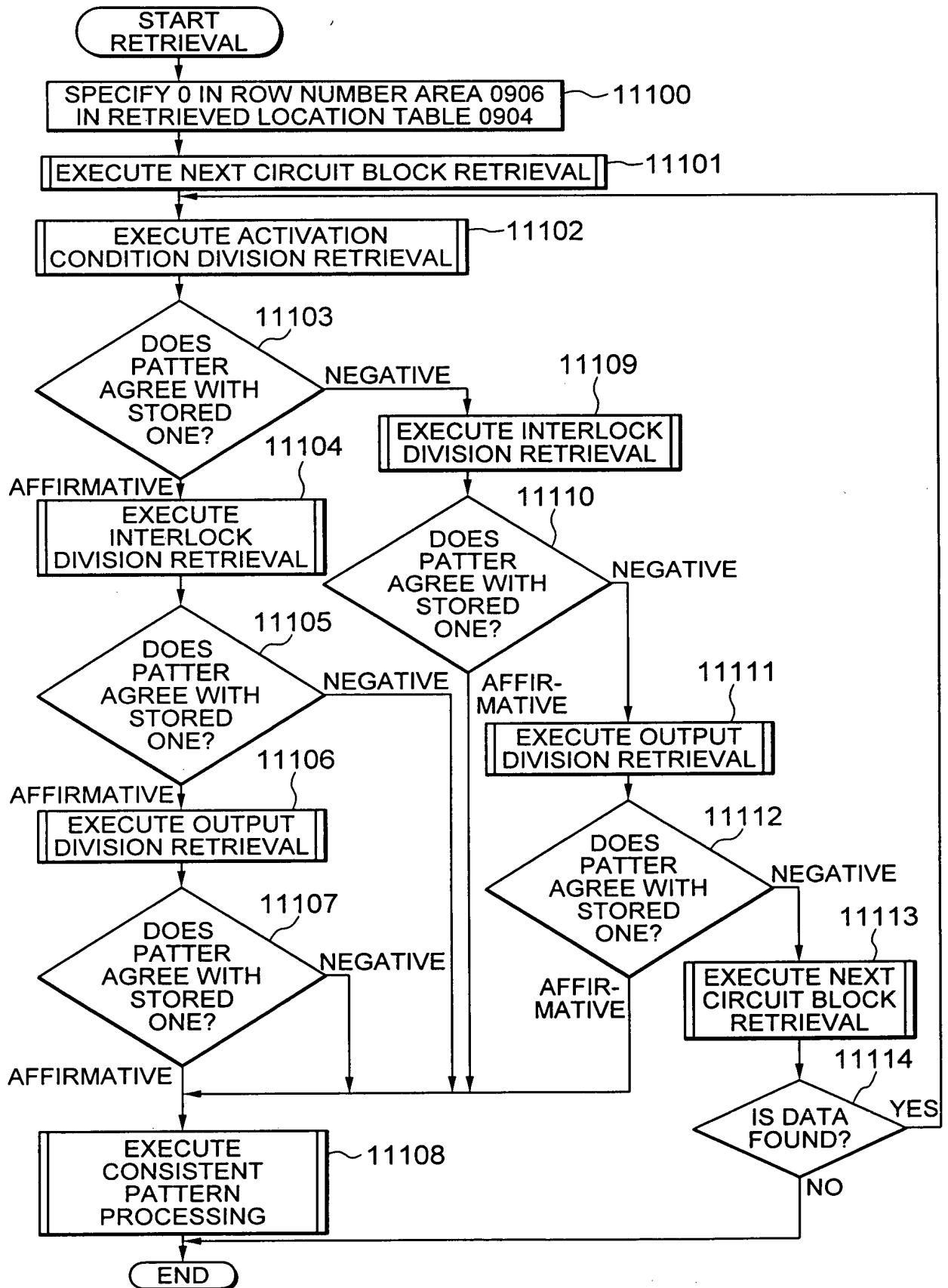
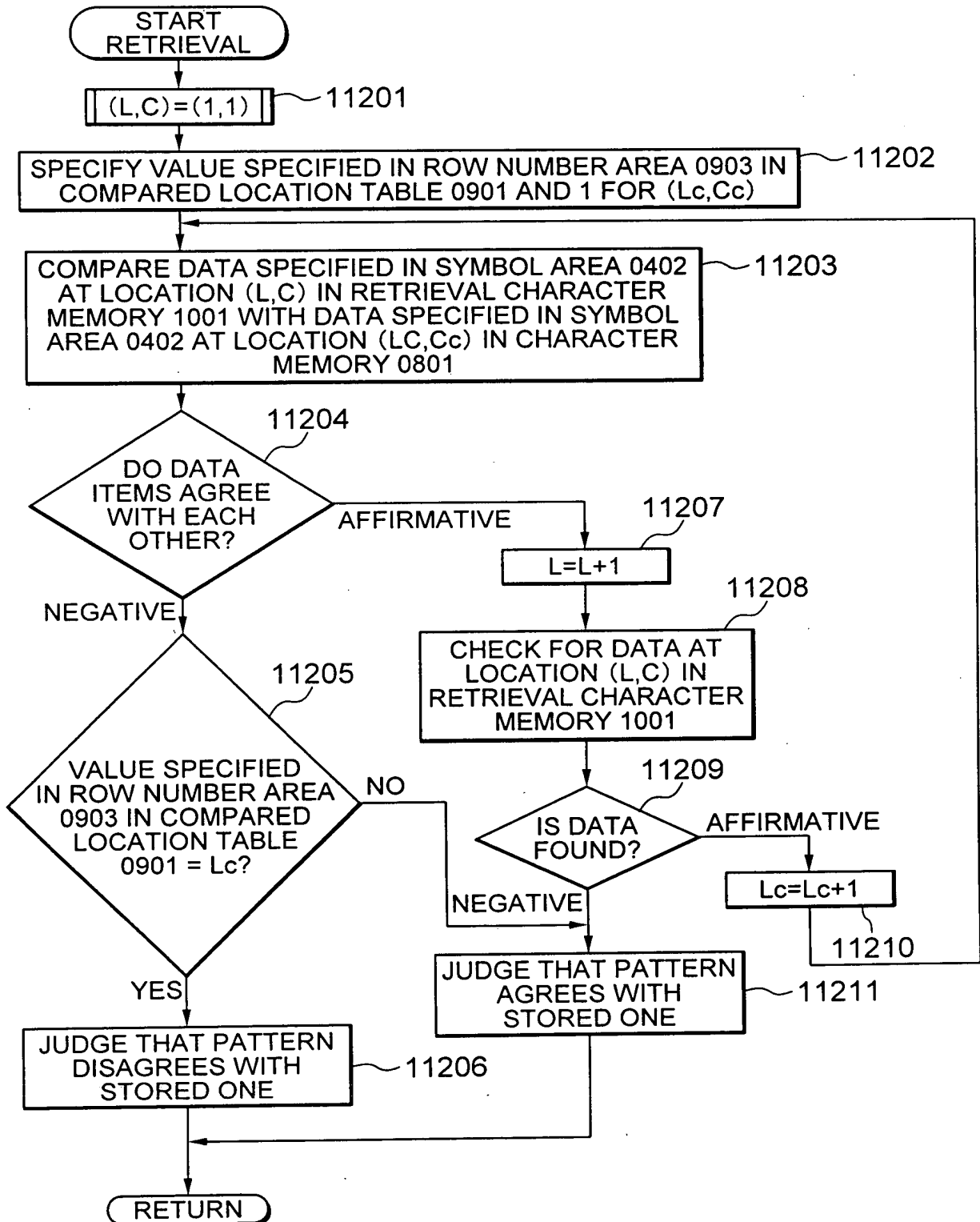


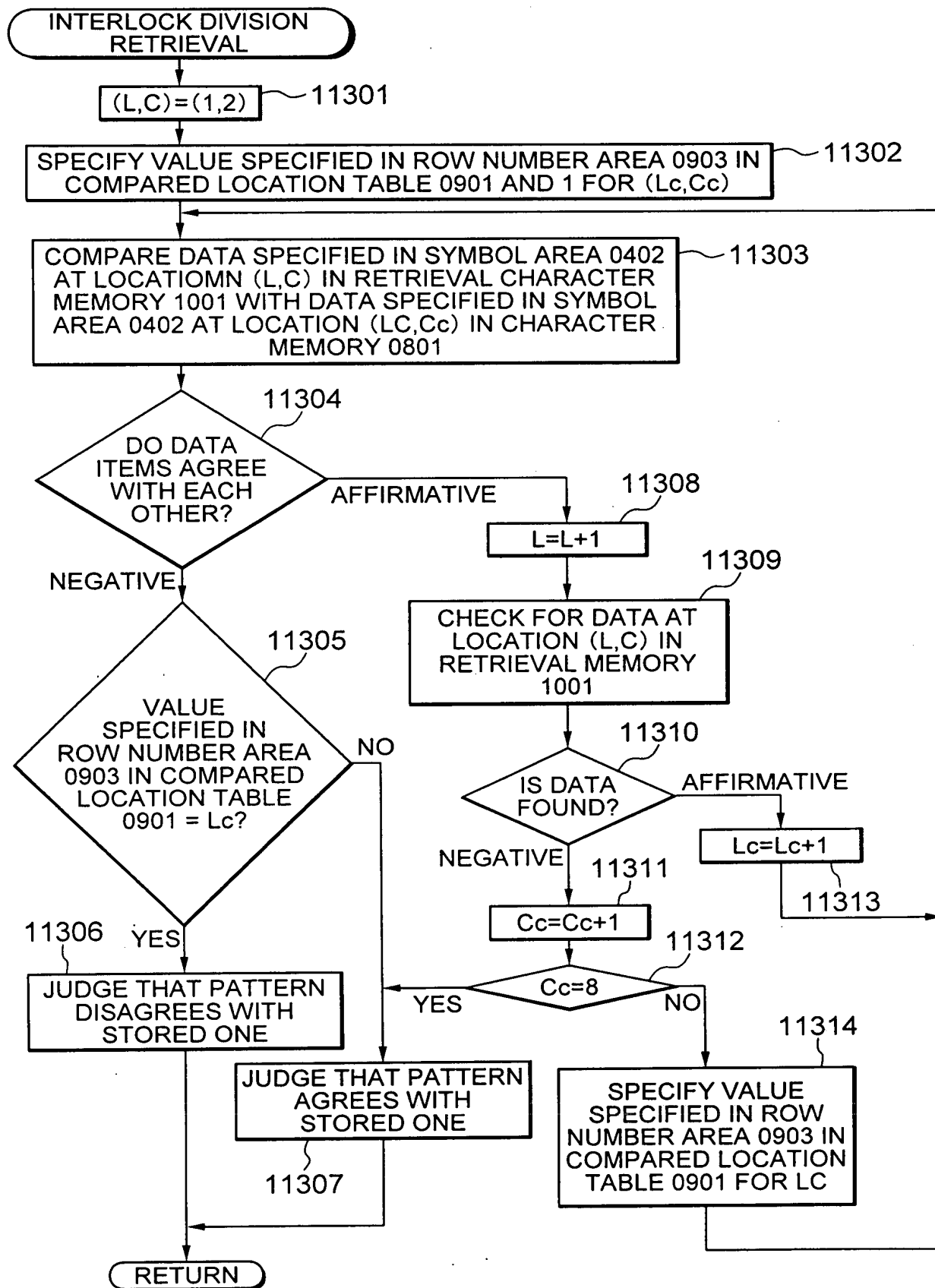


FIG. 15

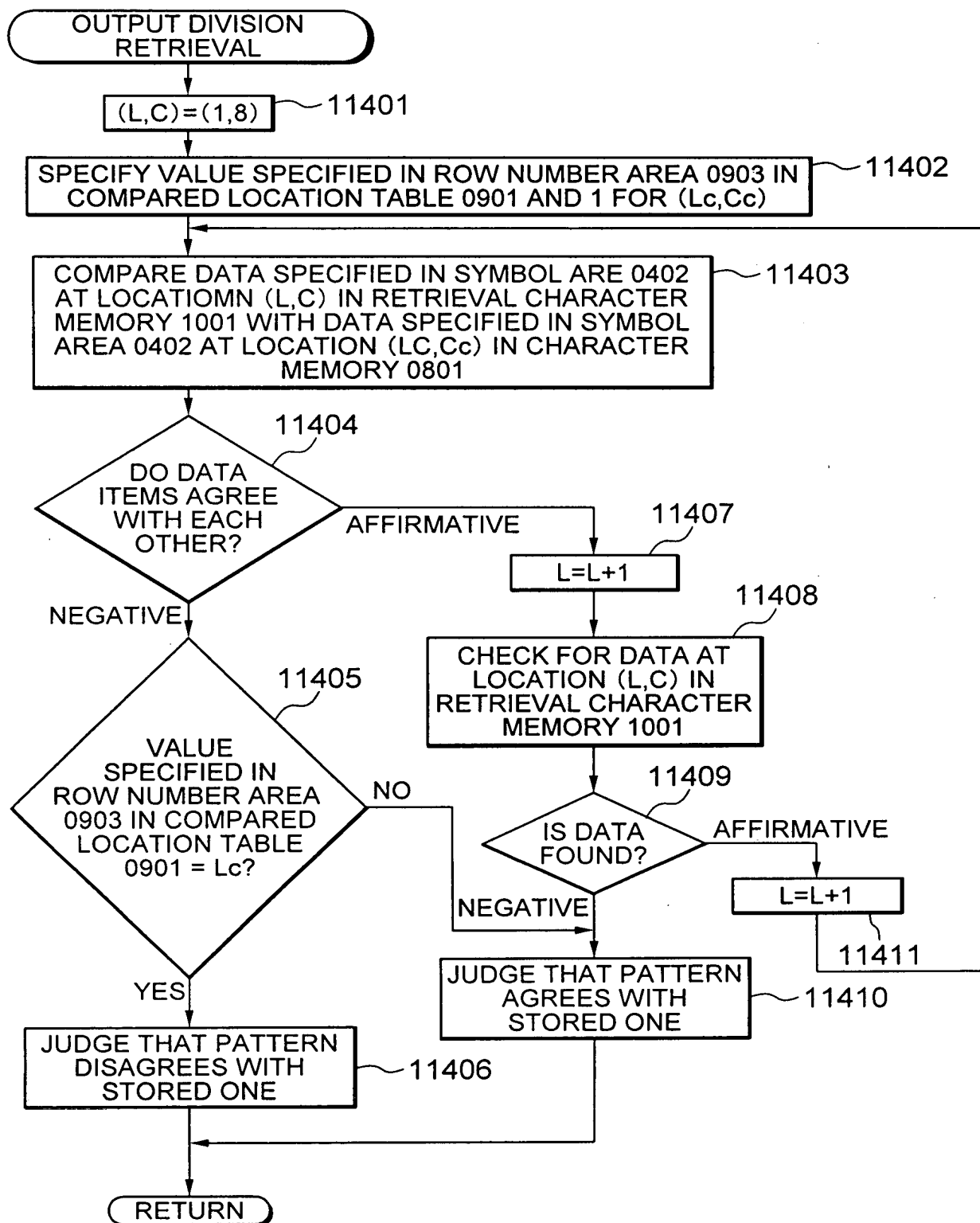


0021 3022250

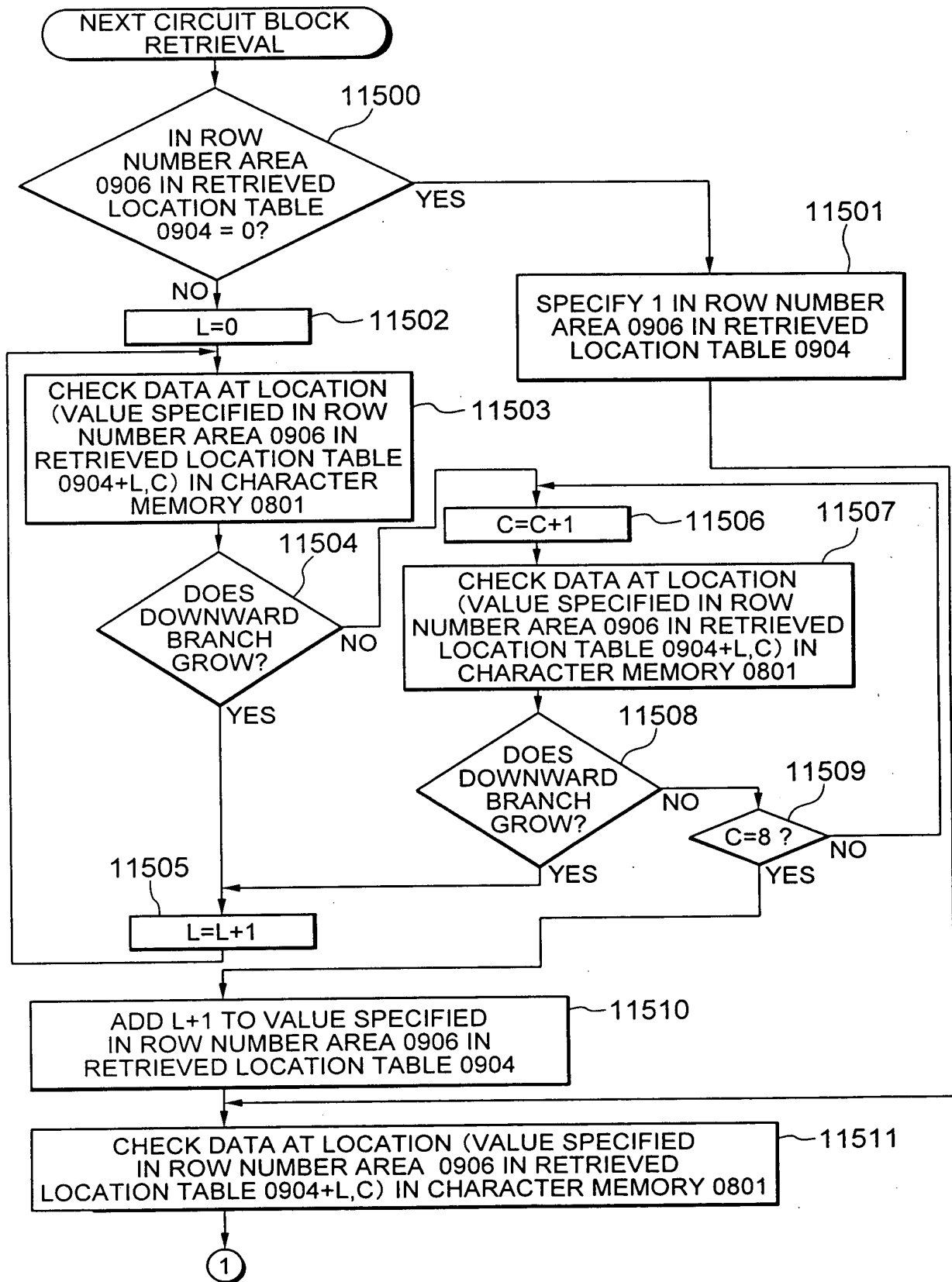
1. The first step is to identify the problem. This involves understanding the symptoms and the context in which they are occurring.



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# FIG. 18A



# FIG. 18B

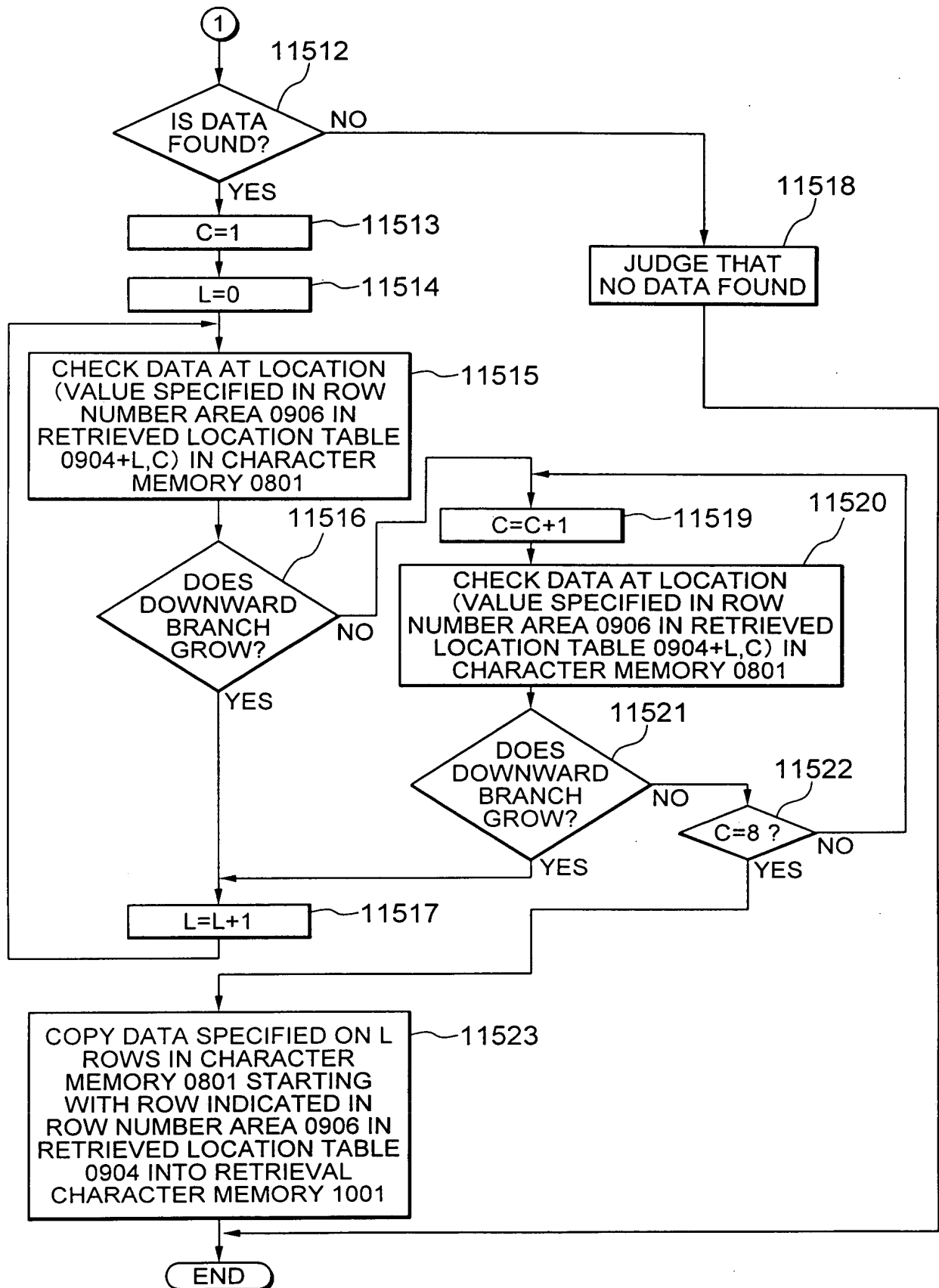




FIG. 20

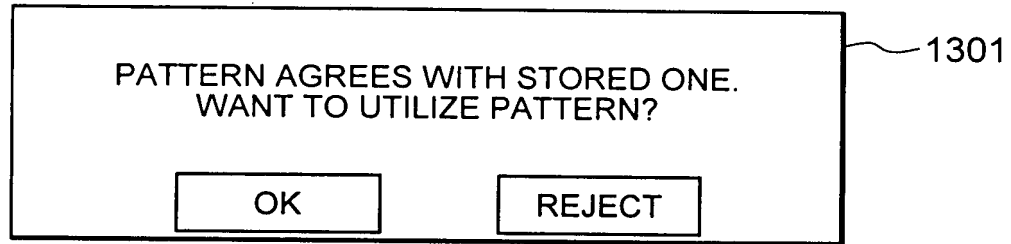


FIG. 21

LADDER DIAGRAM 1310

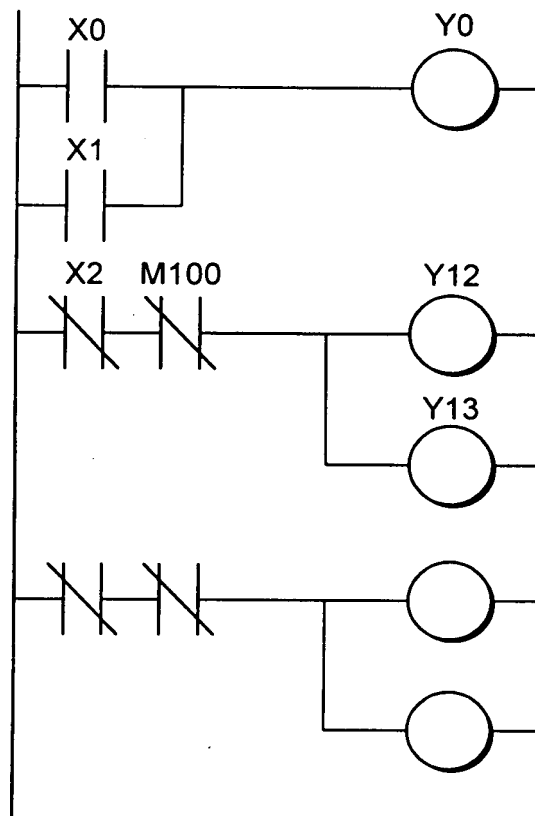


FIG. 22

CHARACTER MEMORY 1311












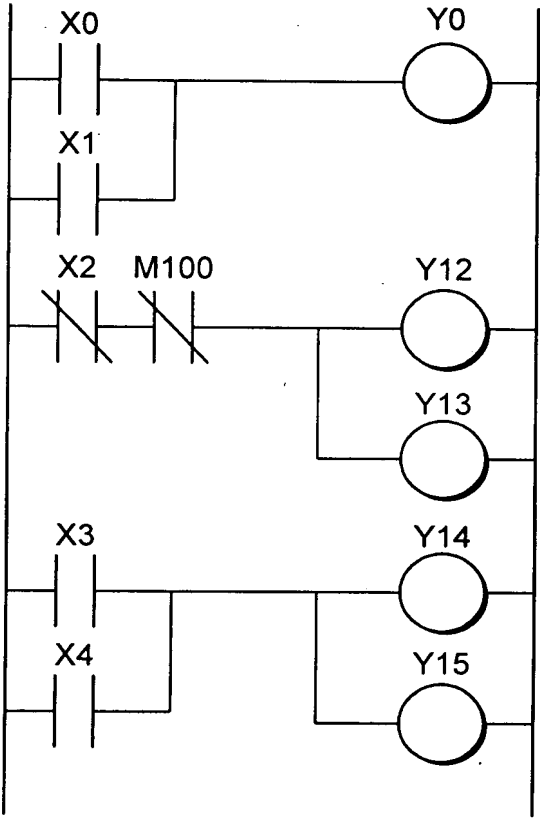
COLUMN		1	2	3	4	5	6	7	8
ROW		X0							Y0
1									
2		X1							
									
3		X2	M100						Y12
									
4									Y13
									
5									
6									
									

FIG. 22



FIG. 23

LADDER DIAGRAM 1401



[illegible]

## CHARACTER MEMORY 1501
































		COLUMN							
		1	2	3	4	5	6	7	8
ROW		X0							Y0
1									
		X1							
2									
		X2	M100						Y12
3									
									Y13
4									
		X3							Y14
5									
		X4							Y15
6									
									

FIG. 25

LADDER DIAGRAM 1401

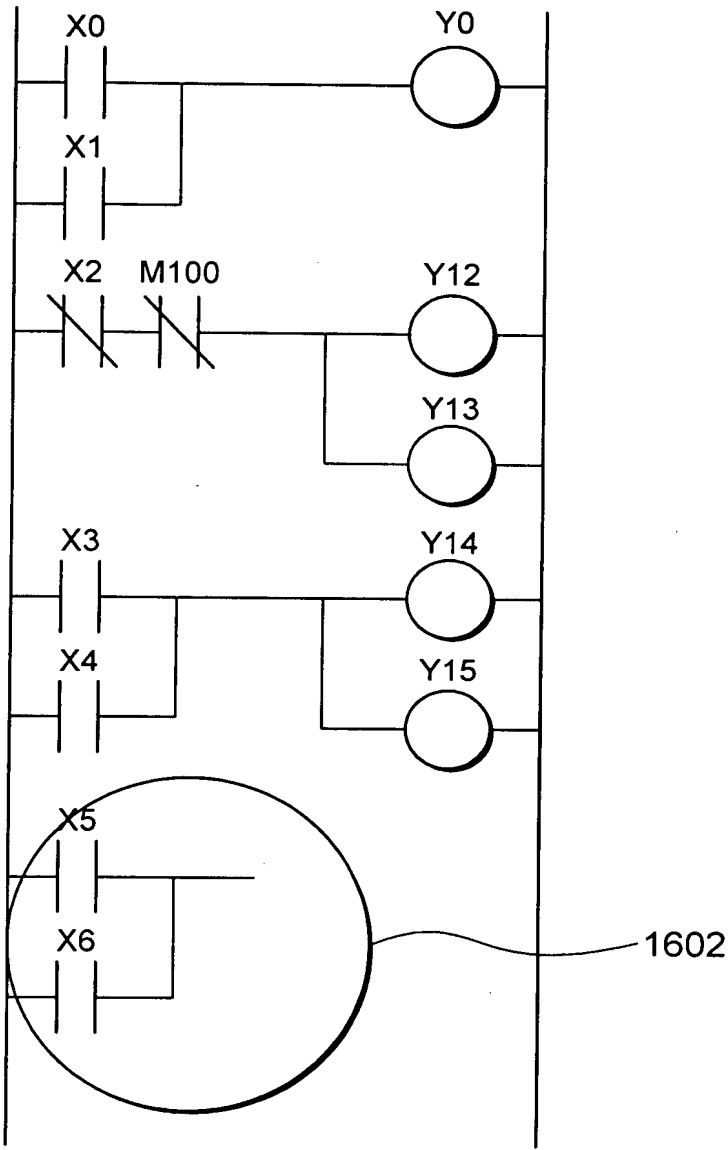
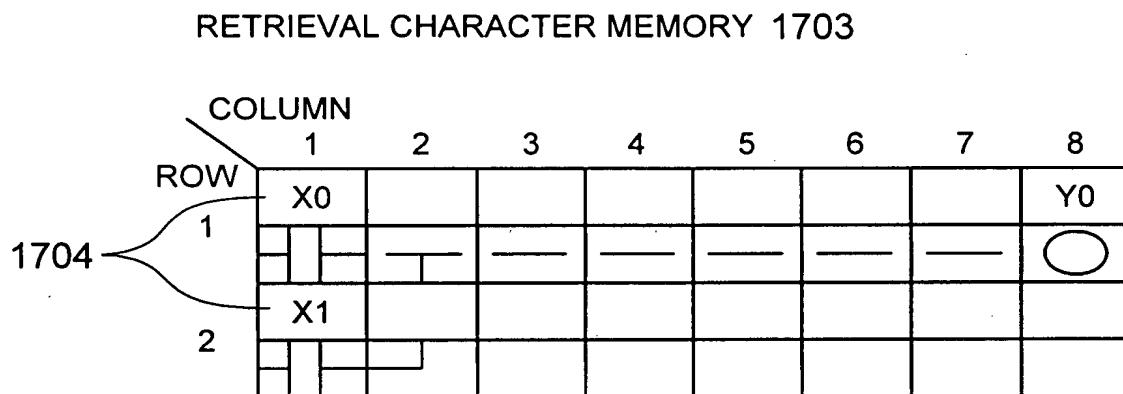


FIG. 26

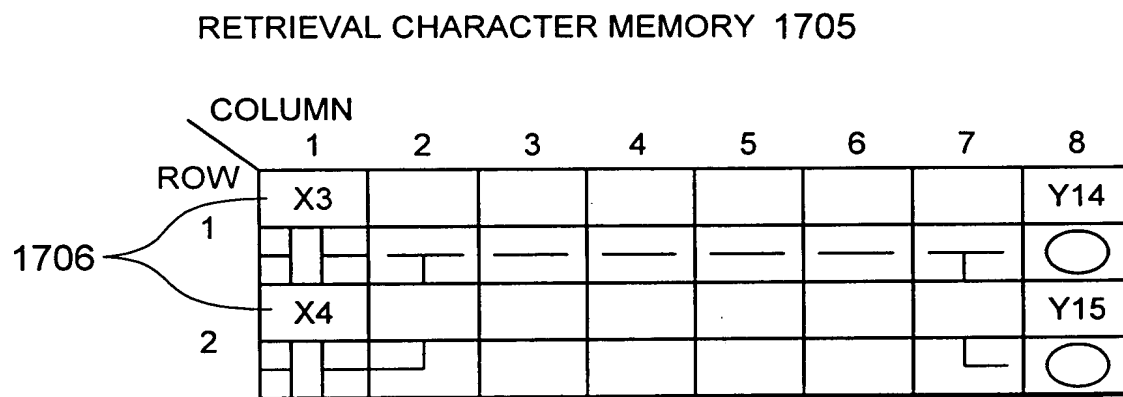
CHARACTER MEMORY

		COLUMN							
		1	2	3	4	5	6	7	8
1701	ROW	X0							Y0
	1								
	2	X1							
	3	X2	M100						Y12
	4								Y13
1702	5	X3							Y14
	6	X4							Y15
	7	X3							
	8	X4							

# FIG. 27



# FIG. 28



# FIG. 29

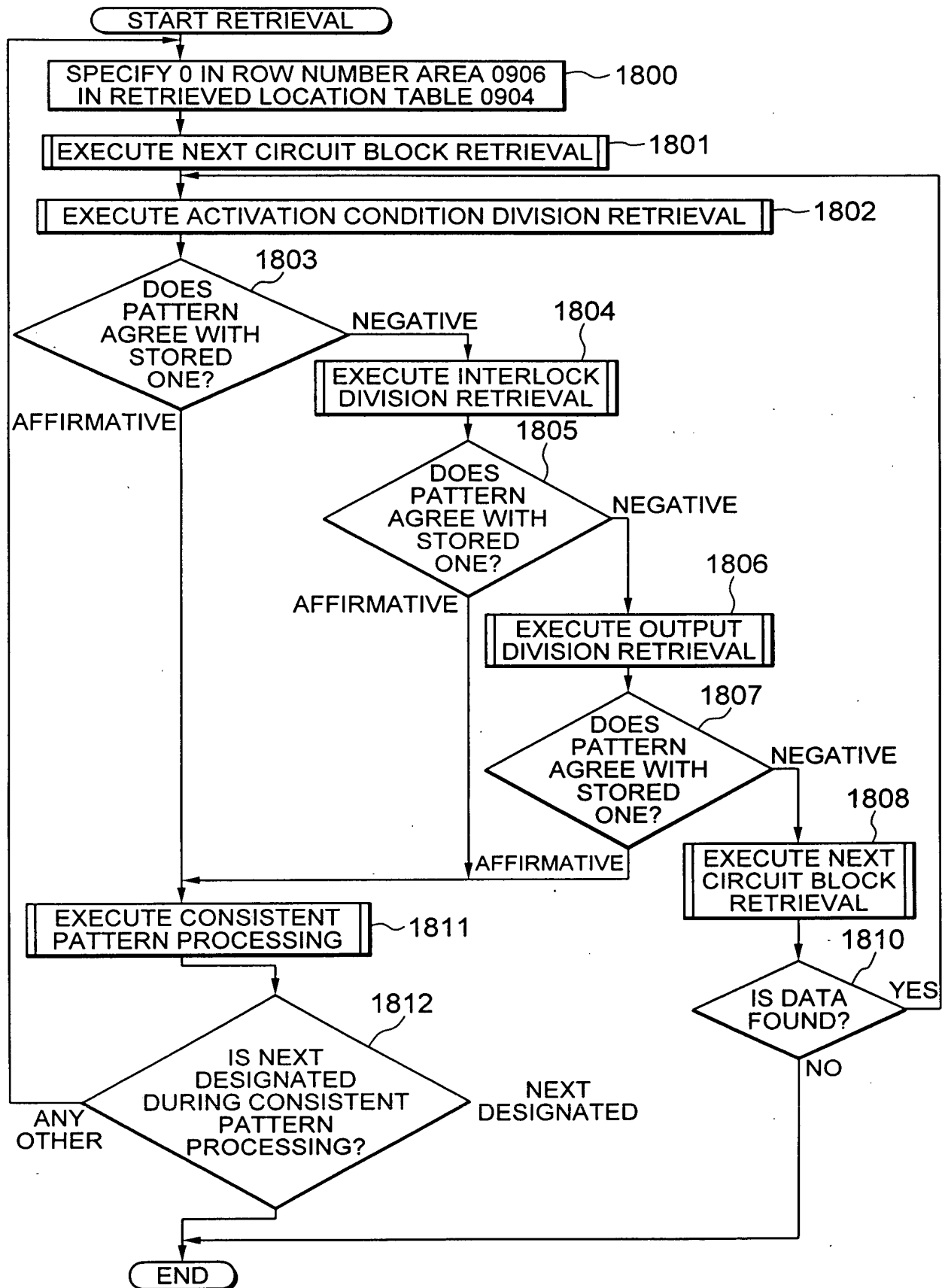


FIG. 30

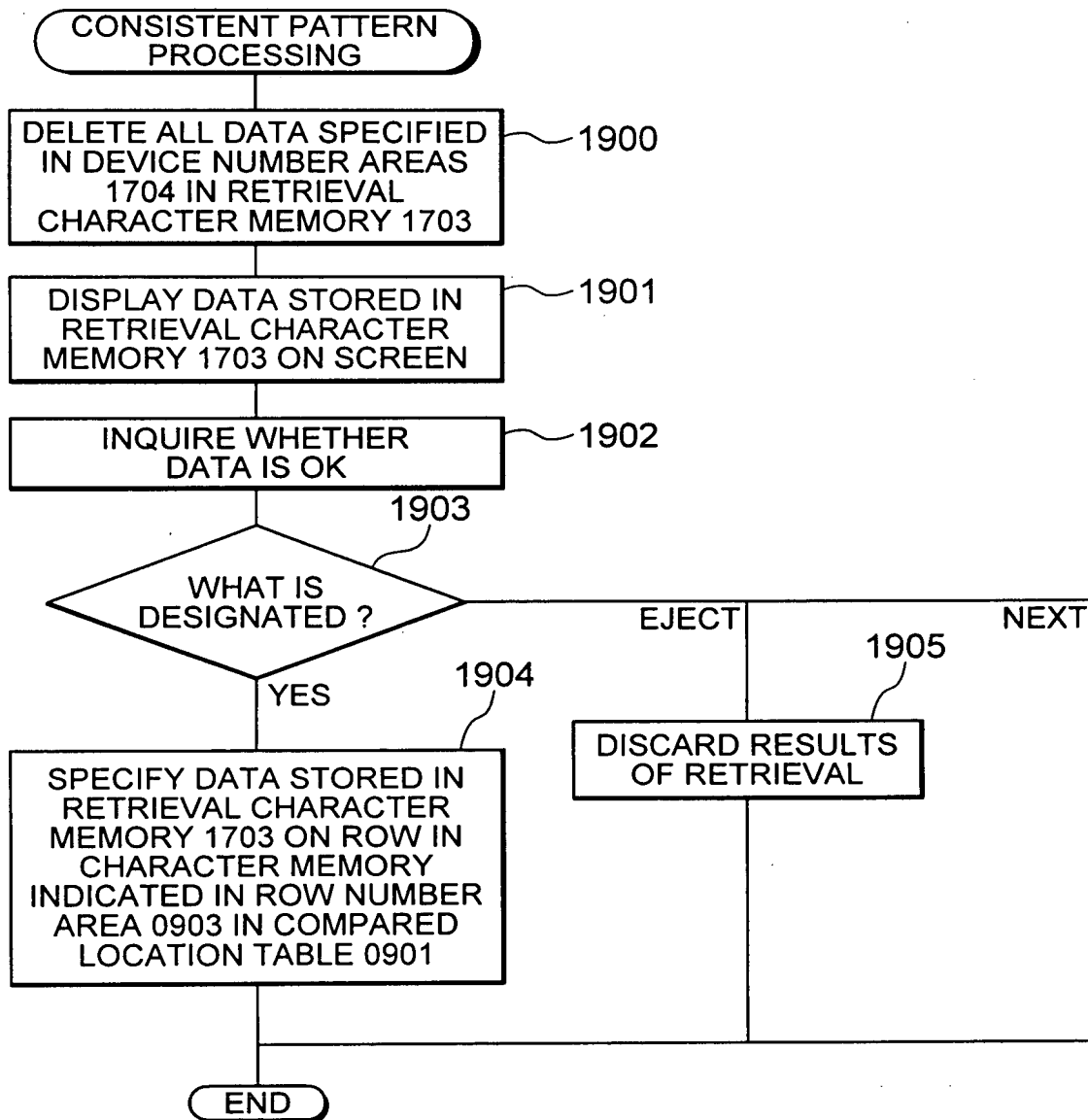


FIG. 31

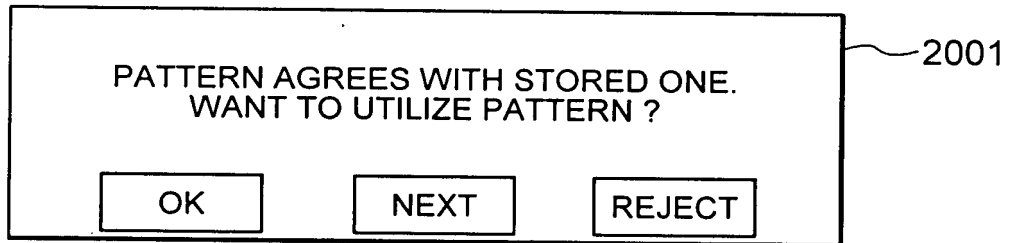
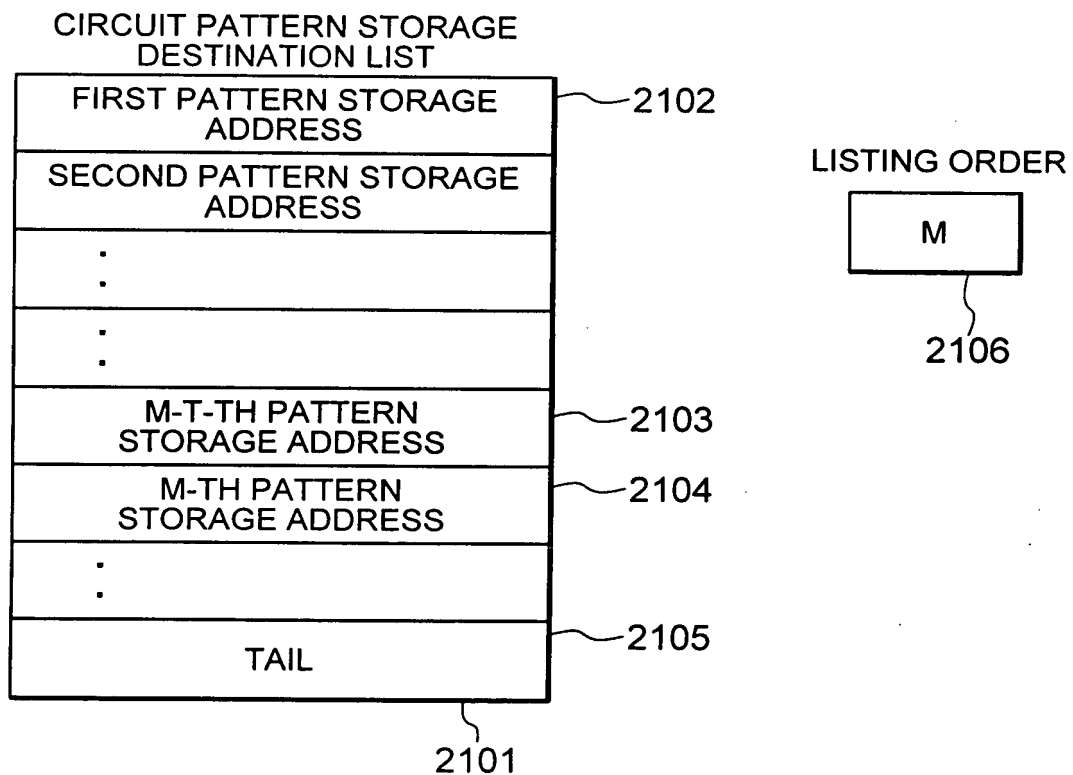












FIG. 32



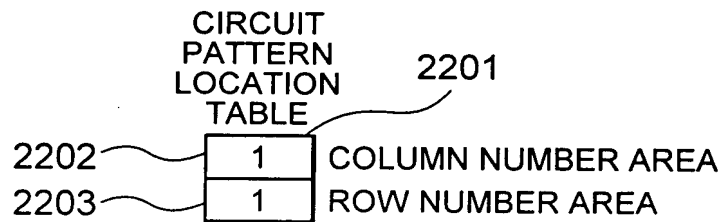


# FIG. 33

		COLUMN							
		1	2	3	4	5	6	7	8
ROW		X1	X2						Y2
									
									Y12
									

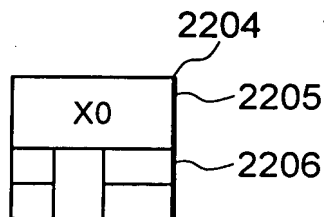
2107

# FIG. 34

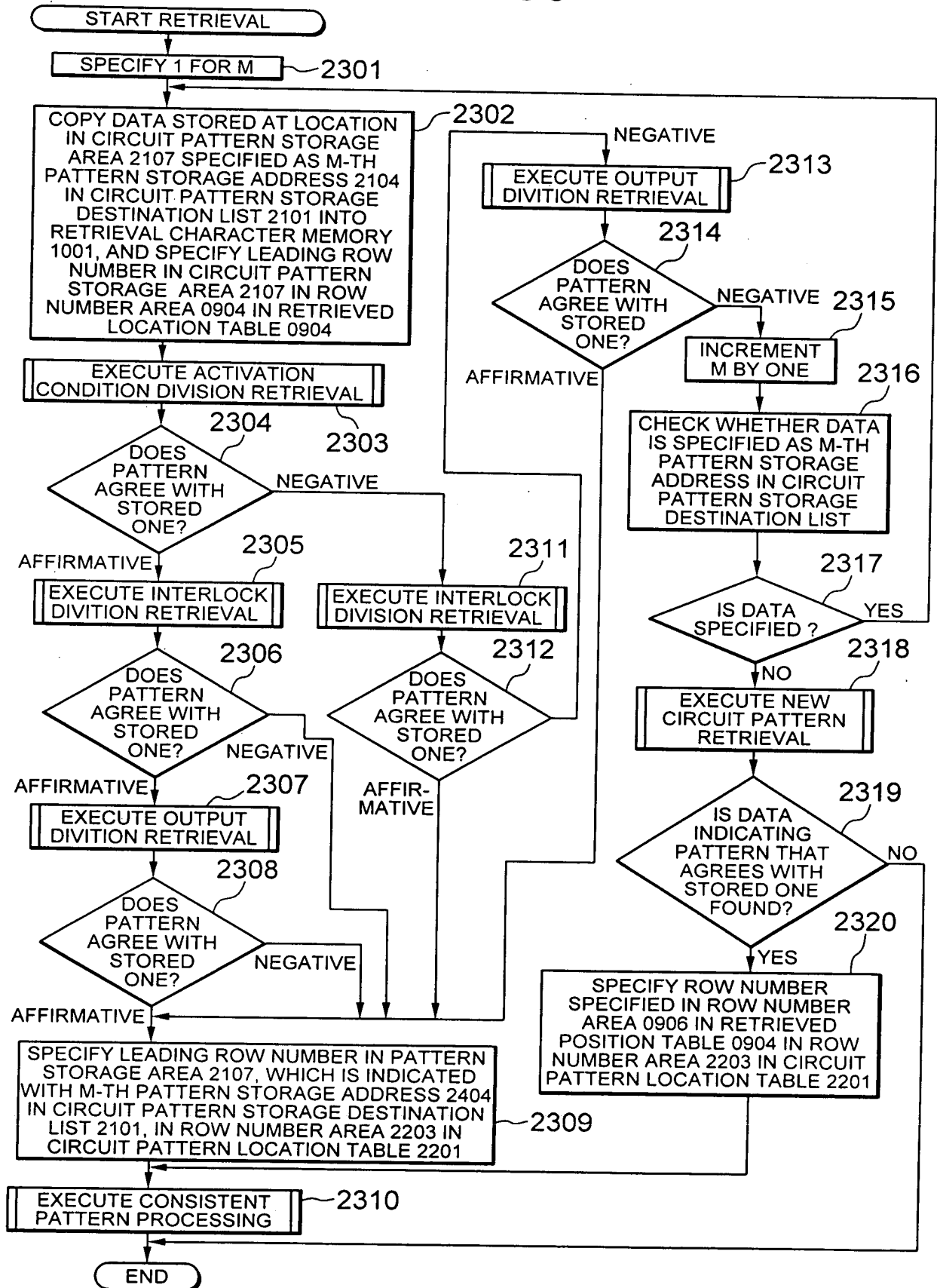


# FIG. 35

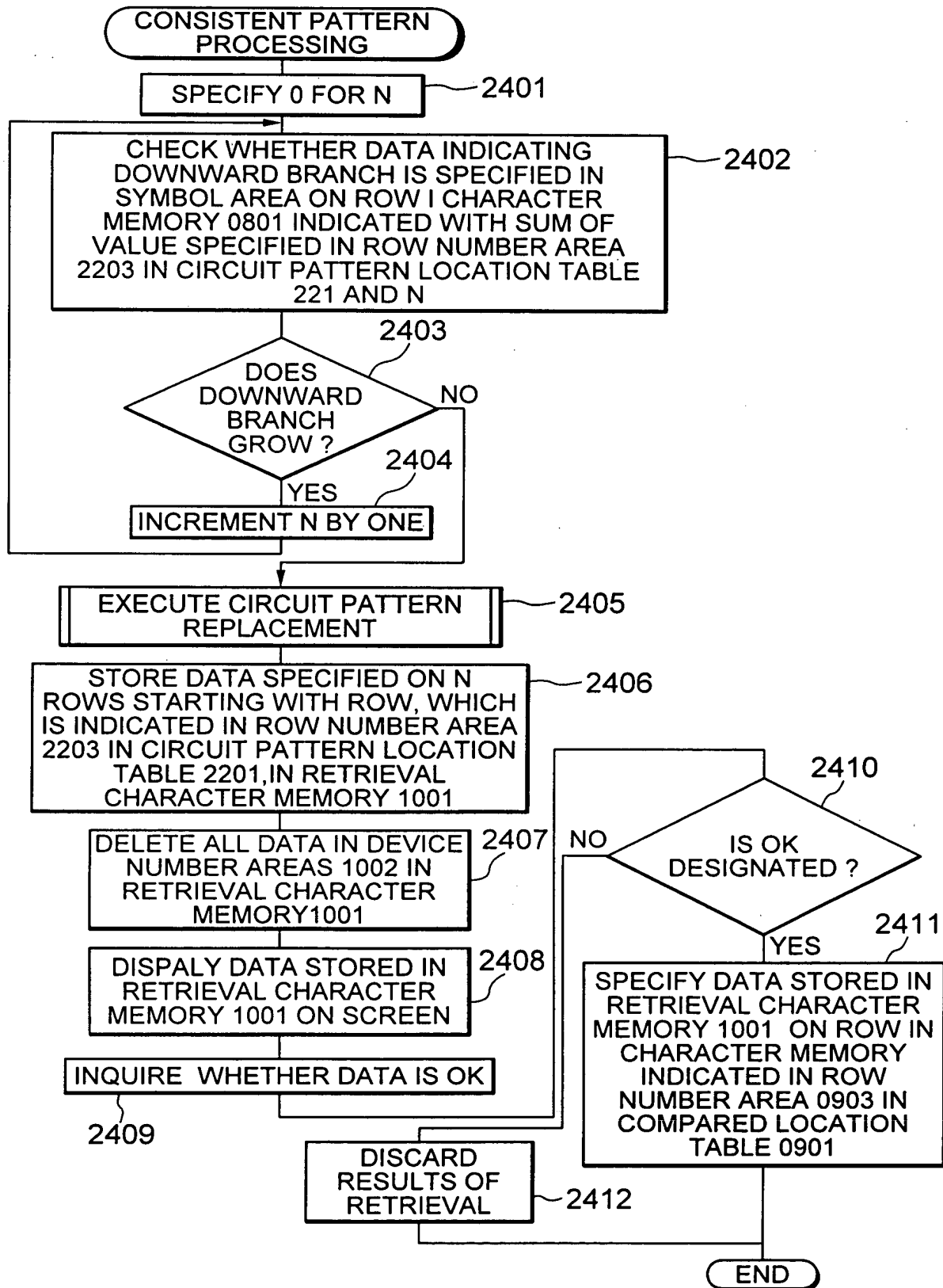
CIRCUIT PATTERN LOCATION CHARACTER MEMORY TABLE



# FIG. 36



# FIG. 37



# FIG. 38

